

Near Real-Time Risk Simulation of Complex Portfolios on Heterogeneous Computing Systems with OpenCL

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- I. Overview: Portfolio Risk
- II. OpenCL + Workload Allocation
- III. Algorithmic Optimizations / Numerical Scheme
- IV. Minimizing Device Global Memory
- V. Conclusion

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Overview: Portfolio Risk

Portfolio Composition:

- **Stocks** (S_1, S_2)
- **Corporate Bond**
- **Foreign Currency**

- **Options:**
 - European Asian (on S_1)
 - European Barrier (on S_2)
 - American Vanilla (on S_1)
 - American 2D Max (on S_1, S_2)

- **Cash** (no simulation required)

What is the **RISK** at which this portfolio is exposed ?

Overview: Portfolio Risk

What is Value-at-Risk (VaR) ?

„I am α percent certain there will not be a loss of more than **VaR** USD in the next N days“ (Hull, *Options Futures and Other Derivatives*).

VaR of level α

Portfolio loss function L

α - quantile

$$Var_{\alpha}(\tilde{L}_{t+N}) \triangleq \inf_{l \in \mathbb{R}} \{P(\tilde{L}_{t+N} > l) \leq (1 - \alpha)\} = \inf_{l \in \mathbb{R}} \{\tilde{F}_L(l) \geq \alpha\}$$

Loss empirical
distribution function

$$\tilde{F}_L(l) \triangleq P(\tilde{L}_{t+N} > l)$$

where α is as high as 95%, 99%, 99.5%.

Industry standard (Basel II and Basel III)

What is Expected Shortfall (ES or cVaR) ?

$$ES_{\alpha}(\tilde{L}_{t+N}) \triangleq \frac{1}{1 - \alpha} \int_{\alpha}^1 Var_{\gamma}(\tilde{L}_{t+N}) d\gamma$$

Overview: Portfolio Risk

How to compute Value-at-Risk (VaR) ?

- 1. Historical simulation:** describes future changes ...
 based on empirical distribution of observed past data.
- 2. Variance-Covariance method:** first-order approximation of the loss function L.
 assumes normally distributed returns.
- 3. Monte Carlo (MC) method:** simulates the loss function L.
 - ✓ Does not rely on historical data.
 - ✓ No need for approximation.
 - ✓ No assumption of a normal distribution.
 - ❖ Computationally intensive problem
 - ❖ Heterogeneous problem
 (many different algorithms involved)


Exploit OpenCL

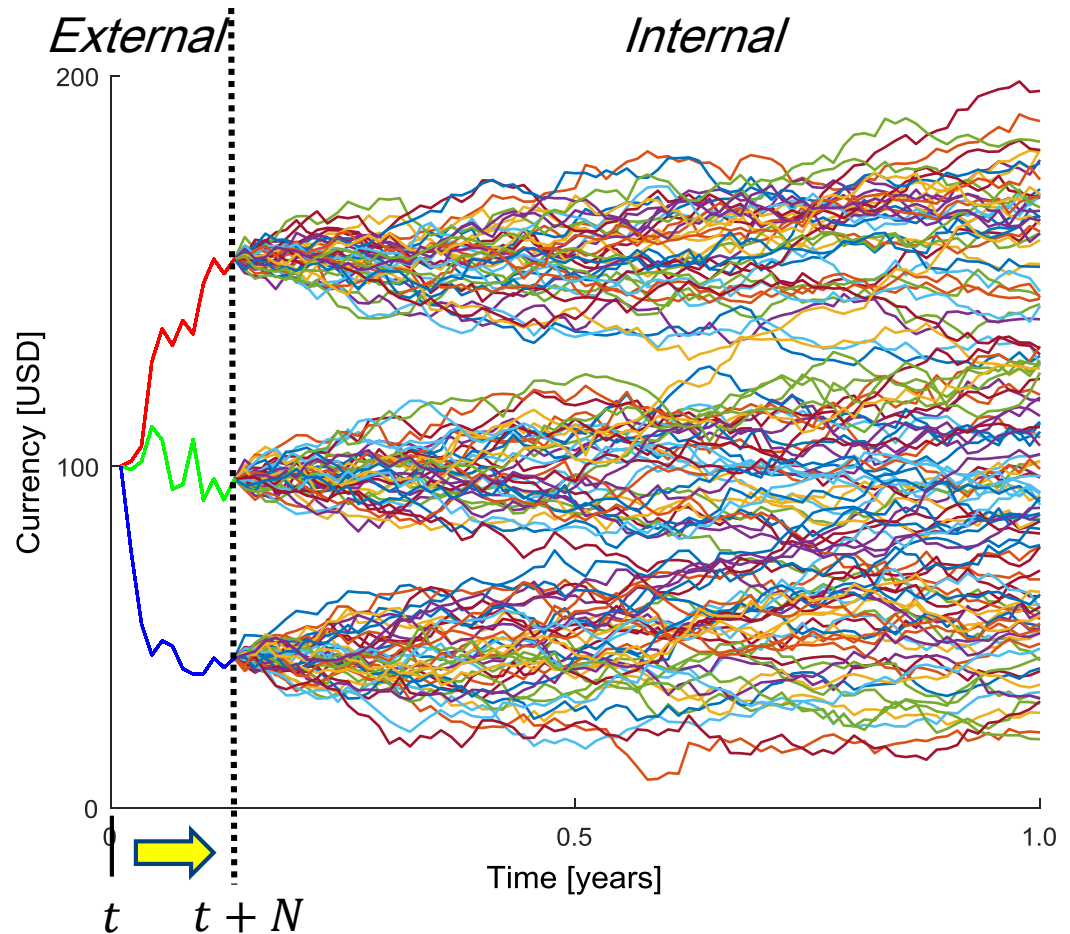
Overview: Portfolio Risk

Portfolio Simulation:

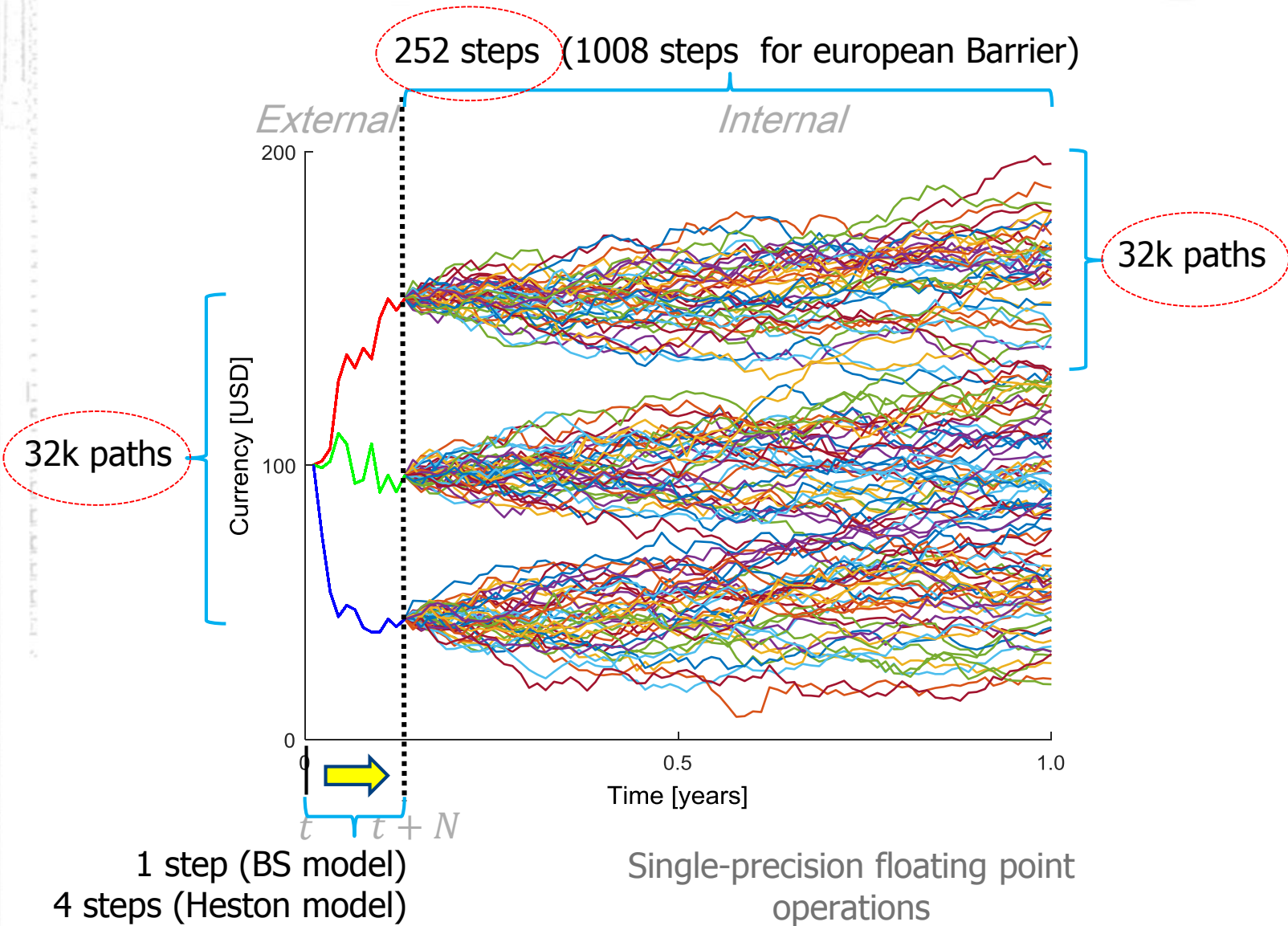
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- **Foreign Currency**
- **Options:**
 - ❑ European Asian (on S_1)
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- **Cash** (no simulation required)

Two Models:

- Black-Scholes (BS)
- Heston

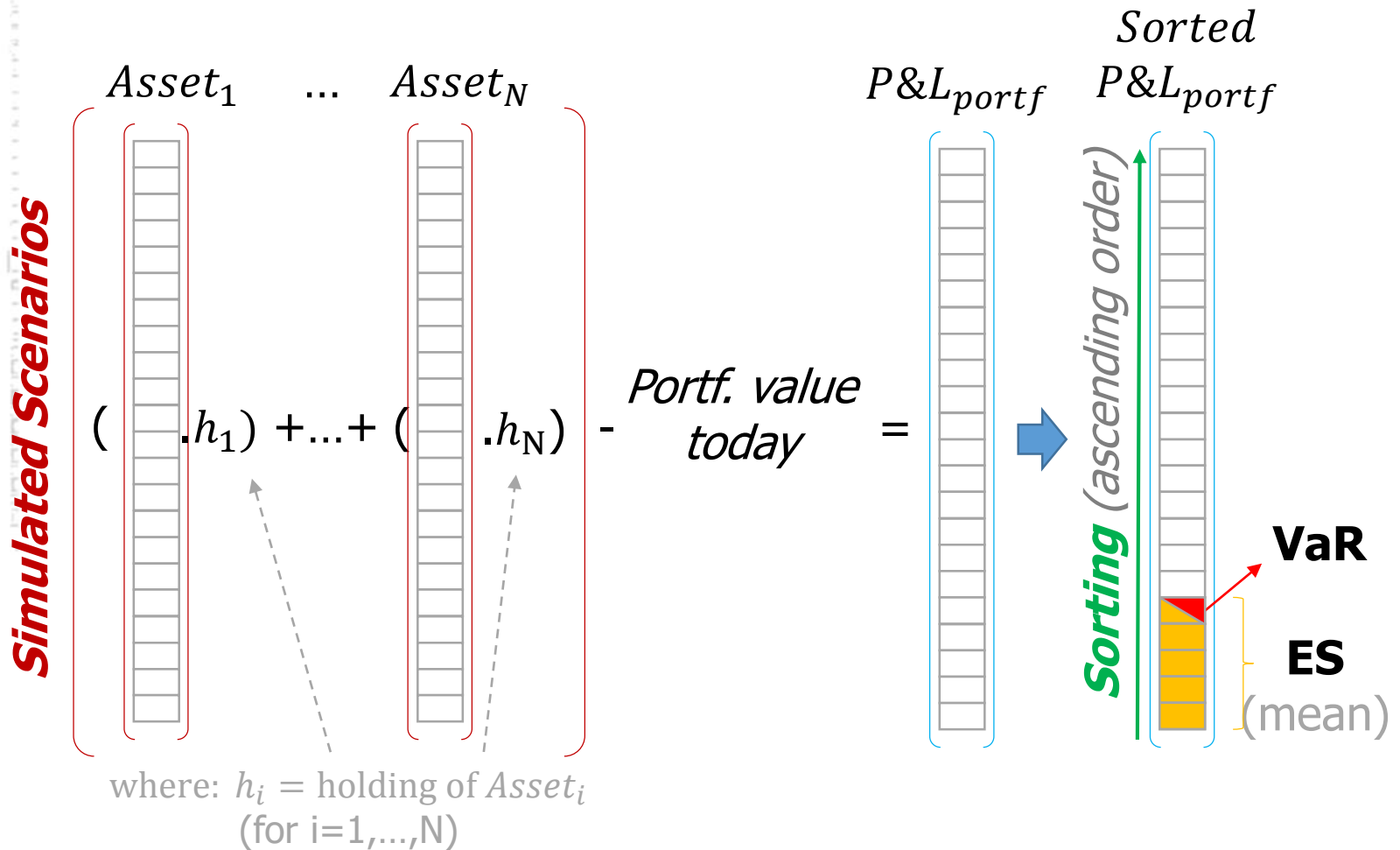


Overview: Portfolio Risk



Overview: Portfolio Risk

Sequence of steps:



I. Overview: Portfolio Risk

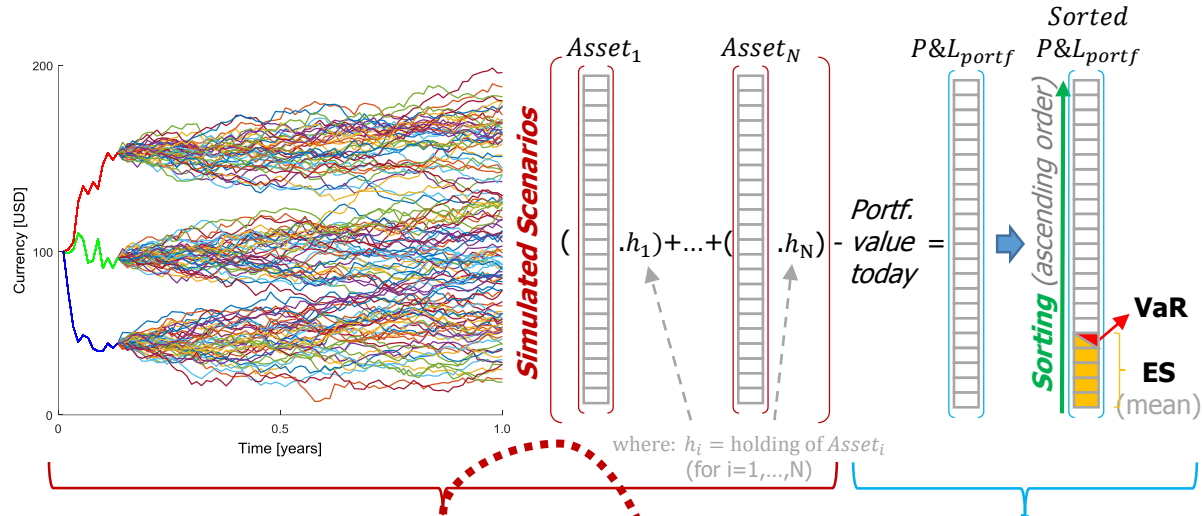
II. OpenCL + Workload Allocation

III. Algorithmic Optimizations / Numerical Scheme

IV. Minimizing Device Global Memory

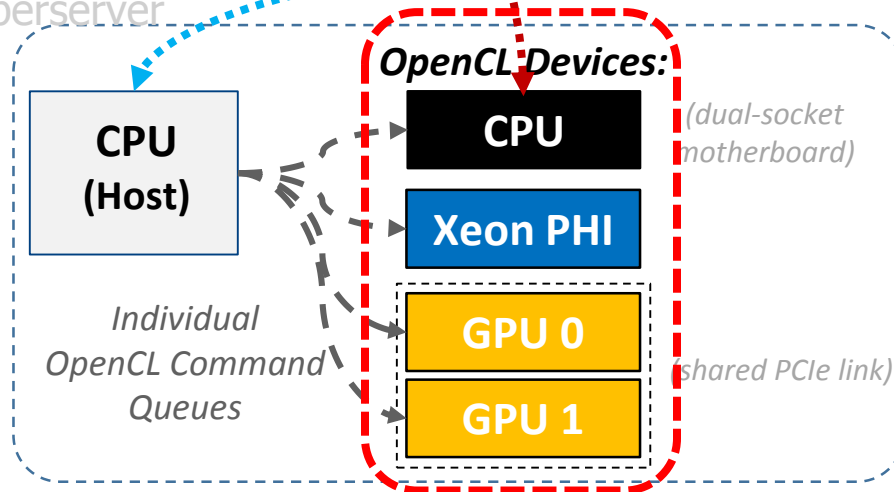
V. Conclusion

OpenCL + Workload Allocation



SuperMicro Superserver
7048GR-TR

Intel Xeon E5-2670 v3



Intel Xeon E5-2670 v3

Intel Xeon Phi 7120P

Nvidia K80

OpenCL + Workload Allocation

ILP formulation

(Matlab's *intlinprog*)

$$\min_x f^T \cdot x \text{ subject to } \begin{cases} x_i \text{ are integers} \\ A \cdot x \leq b \\ A_{eq} \cdot x \leq b_{eq} \\ lb \leq x \leq lu \end{cases}$$

Set of Kernels: $K = \{k_{Stocks2Corr}, k_{Bond}, k_{F.Currency}, \dots\}$

Set of Devices: $D = \{d_{CPU}, d_{PHI}, d_{GPU0}, \dots\}$

Binary decision variables: $x_{k,d}$, where: $0 \leq x_{k,d} \leq 1$

Total runtime: x_T , where: $0 \leq x_T \leq \infty$

OpenCL + Workload Allocation

ILP formulation ... (continued)

Constraints:

Single assignment of each kernel among all devices:

$$\forall k \in K, \sum_{d \in D} x_{k,d} = 1$$

Device runtime:

$$\forall d \in D, \sum_{k \in K} t_{k,d} \cdot x_{k,d} - x_T \leq 0$$

Device global memory:

$$\forall d \in D, \sum_{k \in K} m_{k,d} \cdot x_{k,d} \leq M_d$$

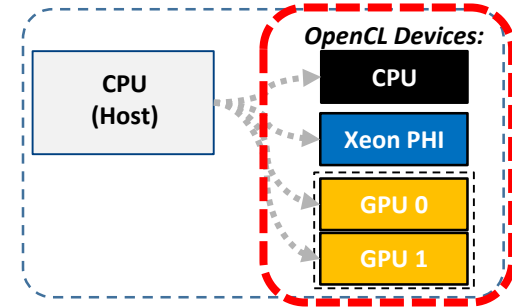
Cost function:

$$f^T \cdot x = x_T$$

OpenCL + Workload Allocation

Workload Allocation with ILP

1) Profile each kernel on every device \rightarrow 2) ILP \rightarrow

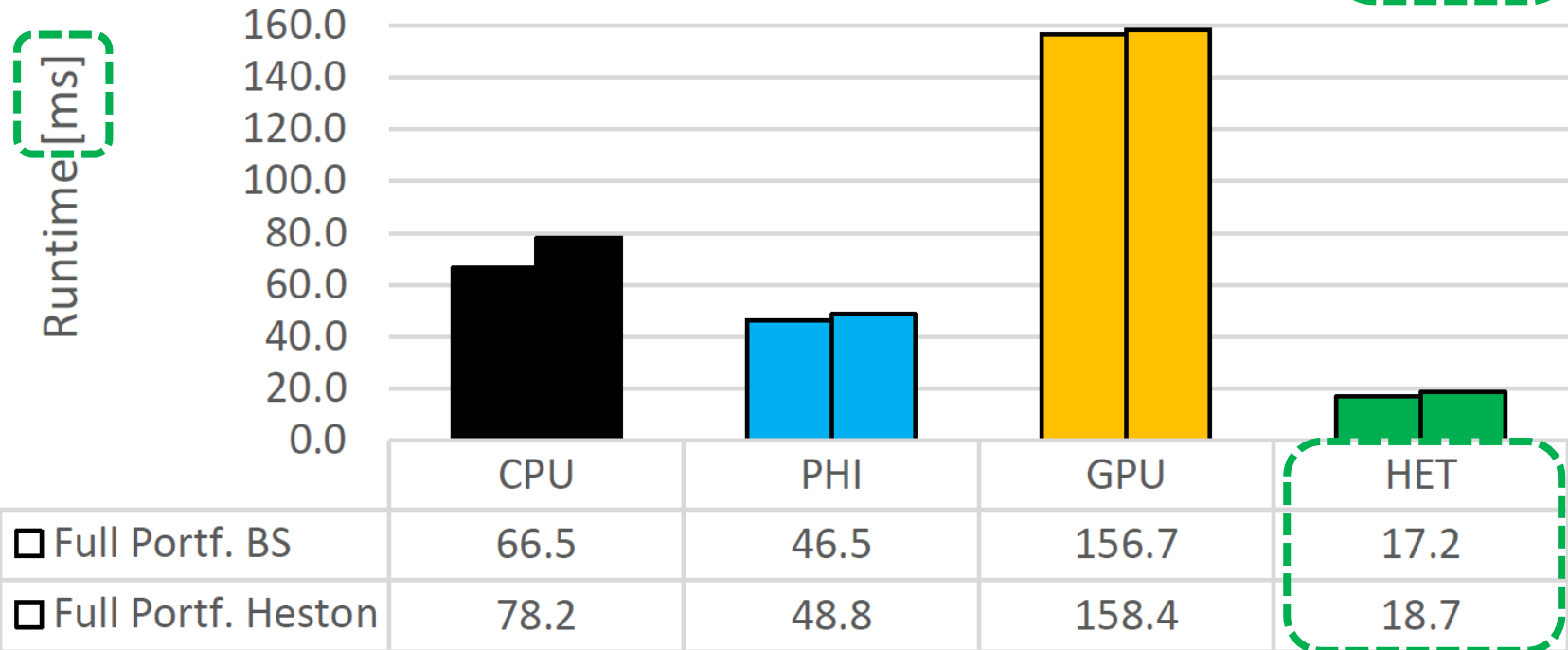
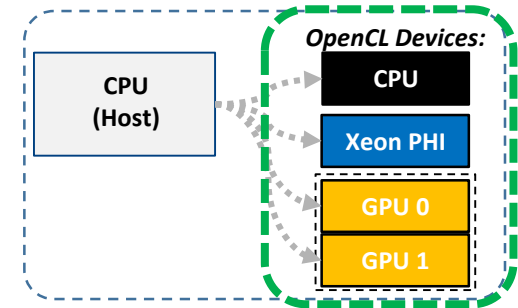


Kernel	Model	Runtime		
		CPU	PHI	GPU
<i>External Simulation</i>				
Stocks2Corr	BS	0.12 ms	0.28 ms	0.02 ms
	Heston	0.15 ms	0.43 ms	0.03 ms
Bond	-	0.22 ms	0.37 ms	0.05 ms
F.Currency	-	0.04 ms	0.30 ms	0.01 ms
<i>Internal Simulation: European Options</i>				
Asian(RNs)	BS	8.89 s	6.59 s	6.74 s
	Heston	17.39 s	11.43 s	11.09 s
Asian(InfoPL)	BS	2.83 ms	1.14 ms	0.28 ms
2DMax(RNs)	Heston	399.24 s	572.37 s	91.92 s
2DMax(PathsP1)	BS	14.31 ms	13.78 ms	0.99 ms
	Heston	19.02 ms	20.52 ms	1.45 ms
2DMax(PathsP2) - SurfGen	-	65.33 s	47.78 s	41.20 s
	-	78.89 s	54.93 s	48.11 s
2DMax.Interp	-	0.05 ms	0.32 ms	0.01 ms

Kernel	CPU	PHI	GPU0	GPU1
Model: BS				
Stocks2Corr	-	-	-	✓
Bond	-	-	-	✓
F. Currency	-	-	-	✓
Euro. Asian (<i>Info-reuse</i>) ¹	-	-	✓	-
Euro. Barrier (<i>Info-reuse</i>) ¹	-	✓	-	-
Amer. Vanilla. BT	✓	-	-	-
Amer.2DMax (<i>Interp+S.Load</i>)	-	-	-	✓
Model: Heston				
Stocks2Corr	✓	-	-	-
Bond	✓	-	-	-
F. Currency	-	-	✓	-
Euro. Asian (<i>Info-reuse</i>) ¹	-	-	-	✓
Euro. Barrier (<i>Info-reuse</i>) ¹	-	✓	-	-
Amer. Vanilla. BT	✓	-	-	-
Amer.2DMax (<i>Interp+S.Load</i>)	-	-	-	✓

OpenCL + Workload Allocation

Kernels Runtime



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Algorithmic Optimizations

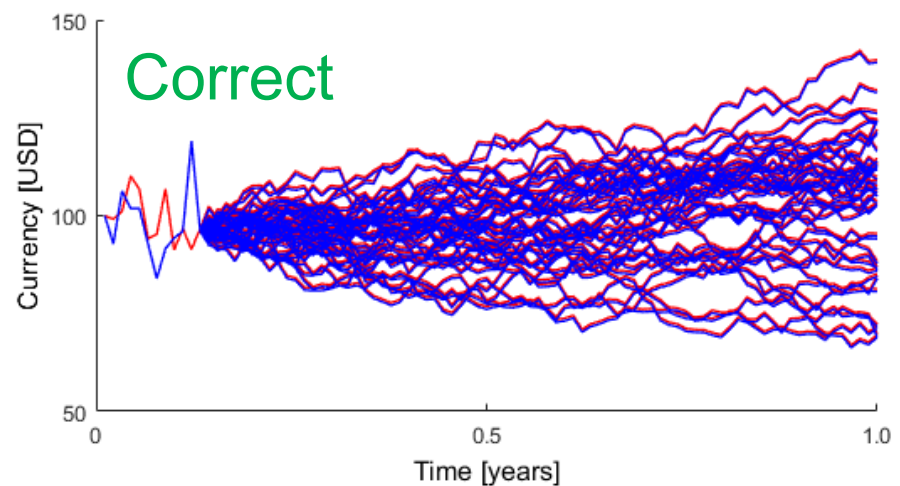
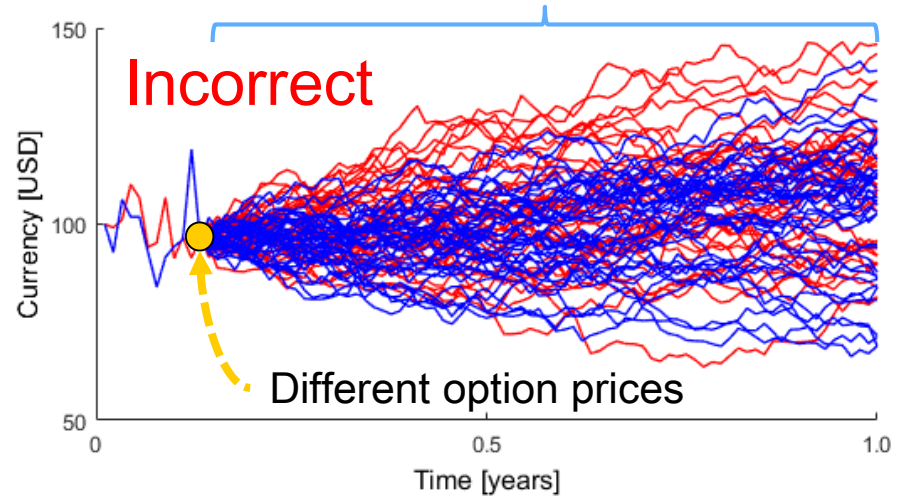
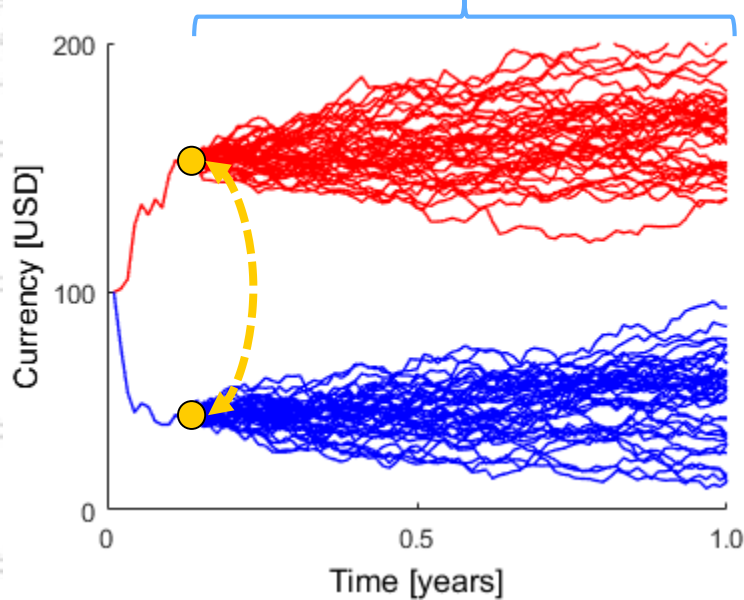
Random Numbers Reuse Approach

Nested MC-Based Risk Measurement of Complex Portfolios: Acceleration and Energy Efficiency

S. Desmettre, R. Korn, J. Varela, N. Wehn.

Risks Vol. 4, no. 4, pages 36, October, 2016.

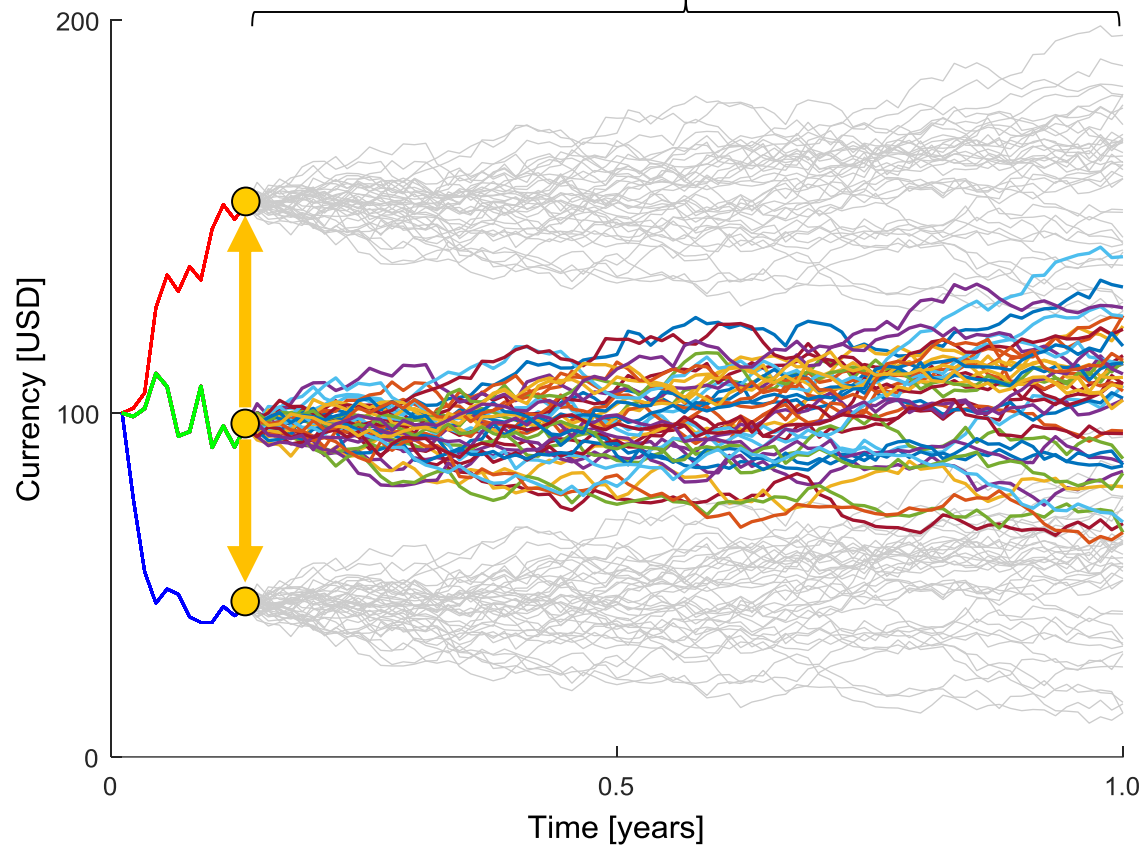
(All parameters in the internal simulation remain constant)



Algorithmic Optimizations

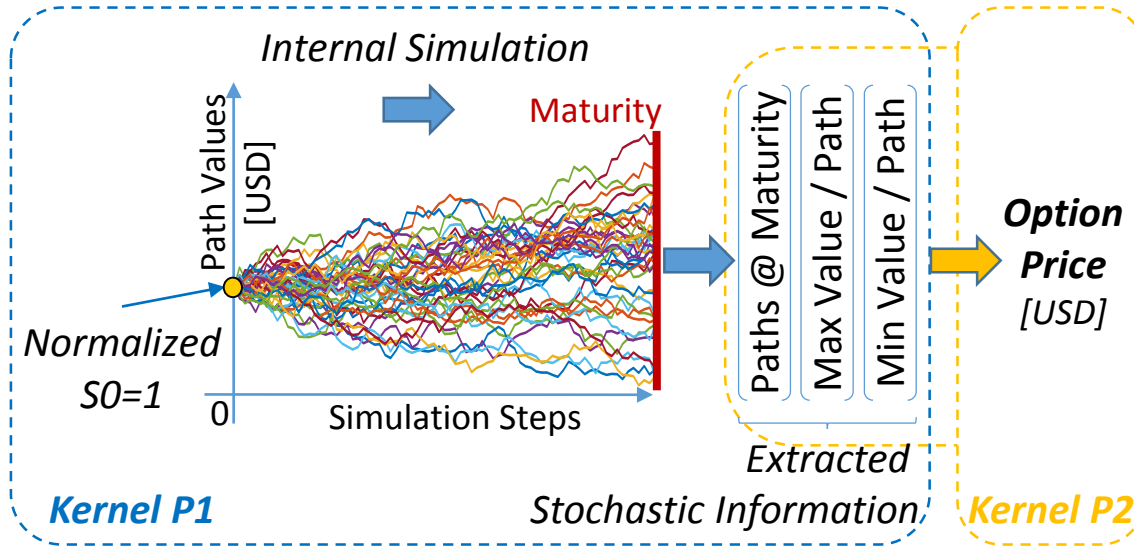
Paths-Reuse Approach (+Normalization)

(Note: All parameters in the internal simulation remain constant)



Algorithmic Optimizations

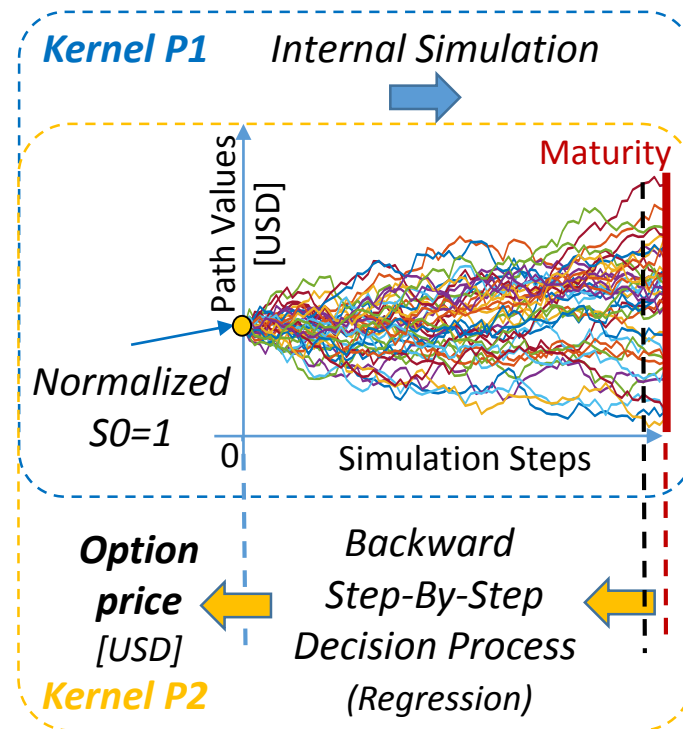
Paths-Reuse: European-style Options



➔ Information-Reuse Approach

Algorithmic Optimizations

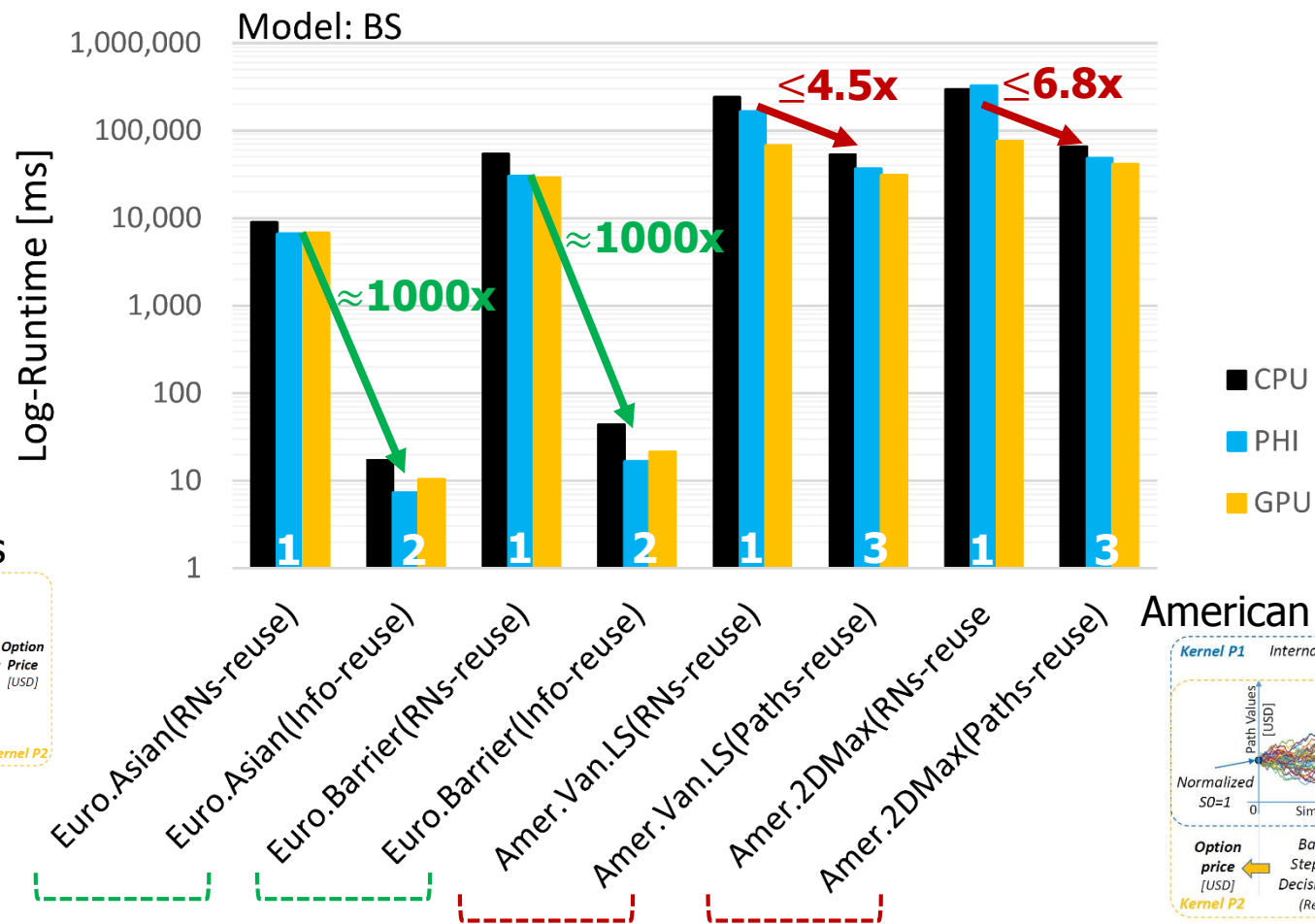
Paths-Reuse: American-style Options



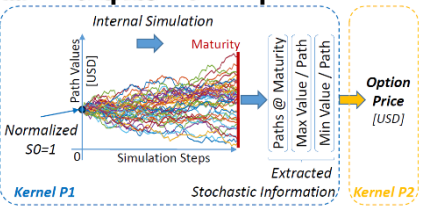
Algorithmic Optimizations

Runtime: Paths-/Info-Reuse (under BS model)

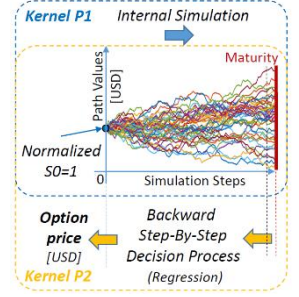
1. RNs-reuse
2. Info-reuse
3. Paths-reuse



European Options



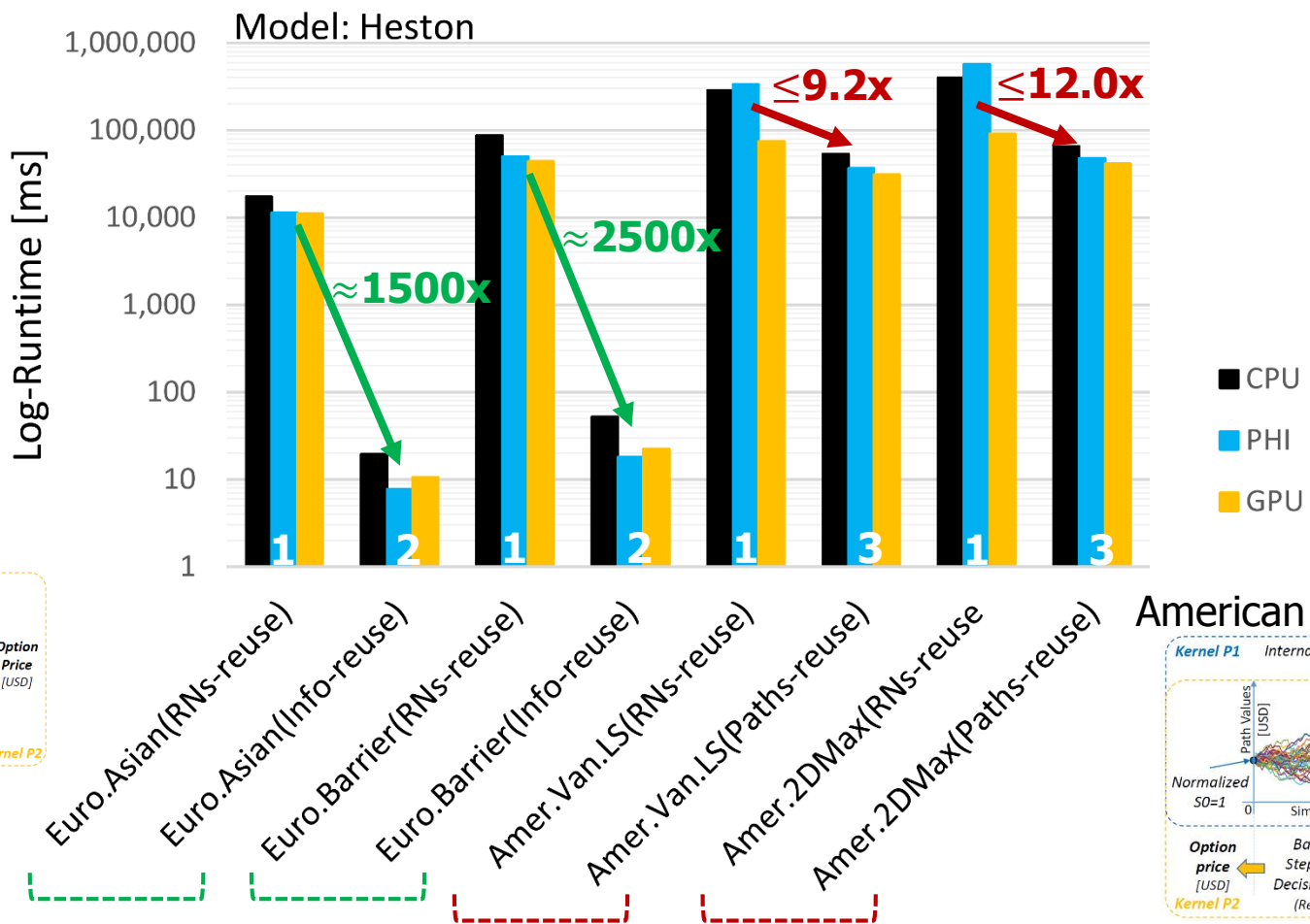
American Options



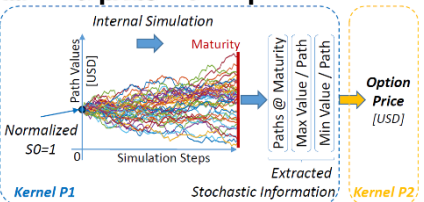
Algorithmic Optimizations

Runtime: Paths-/Info-Reuse (under BS model)

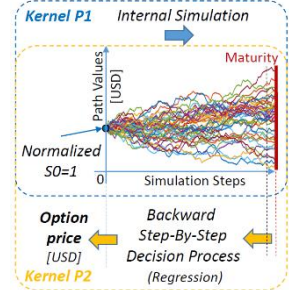
1. RNs-reuse
2. Info-reuse
3. Paths-reuse



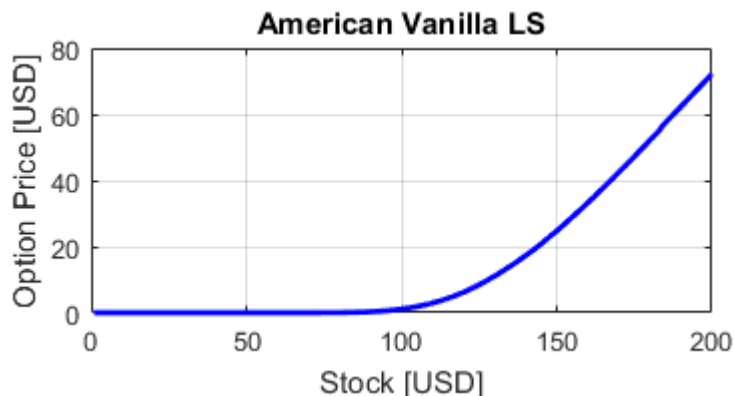
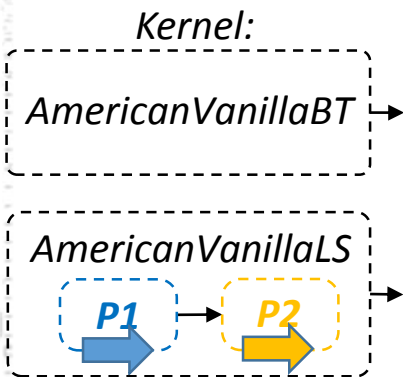
European Options



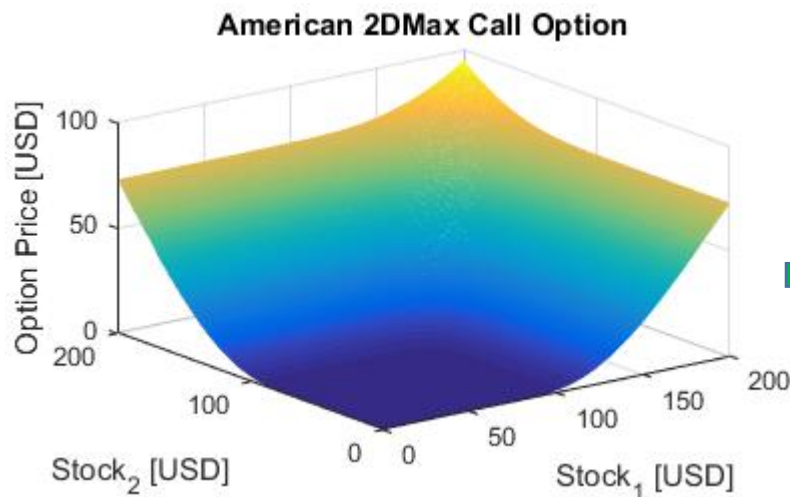
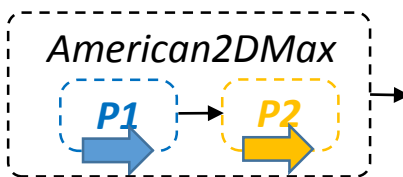
American Options



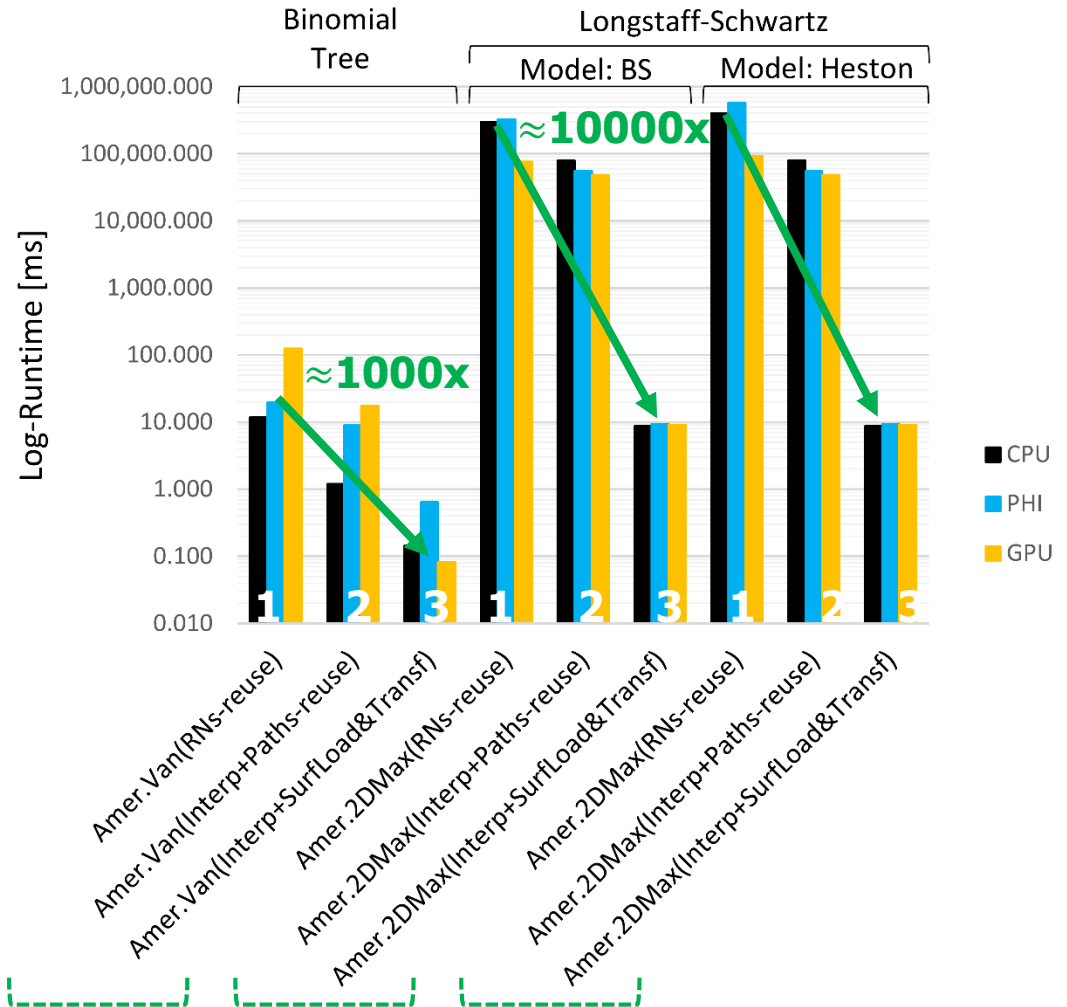
Interpolation:



Extension:



Runtime: Interpolation (American-style Options)

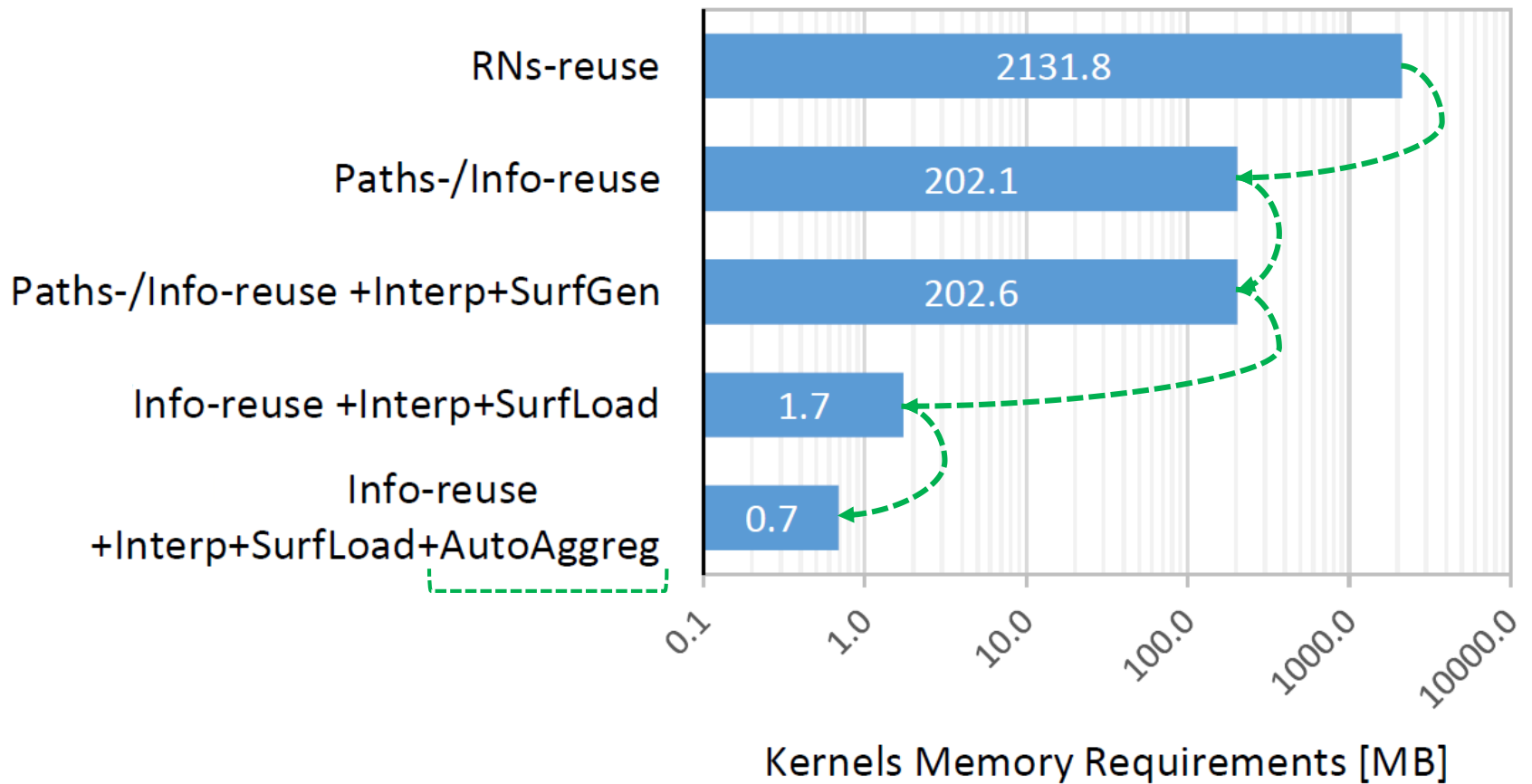


1. RNs-reuse
2. Interpolation+Paths-reuse
3. Interpolation+Surface Load&Transfer

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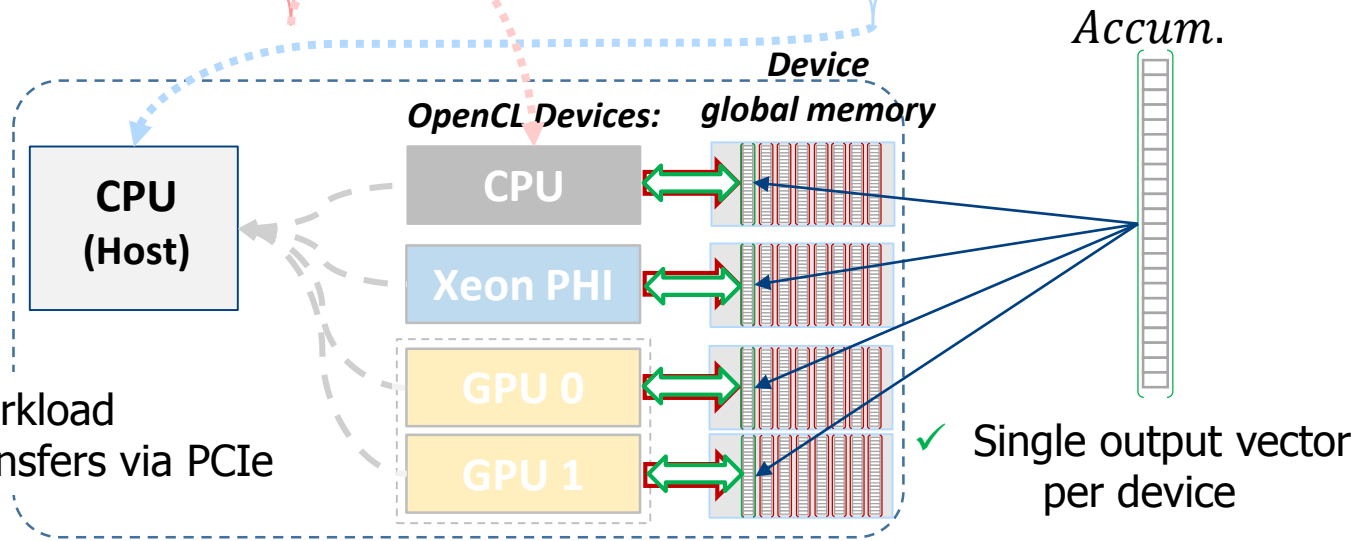
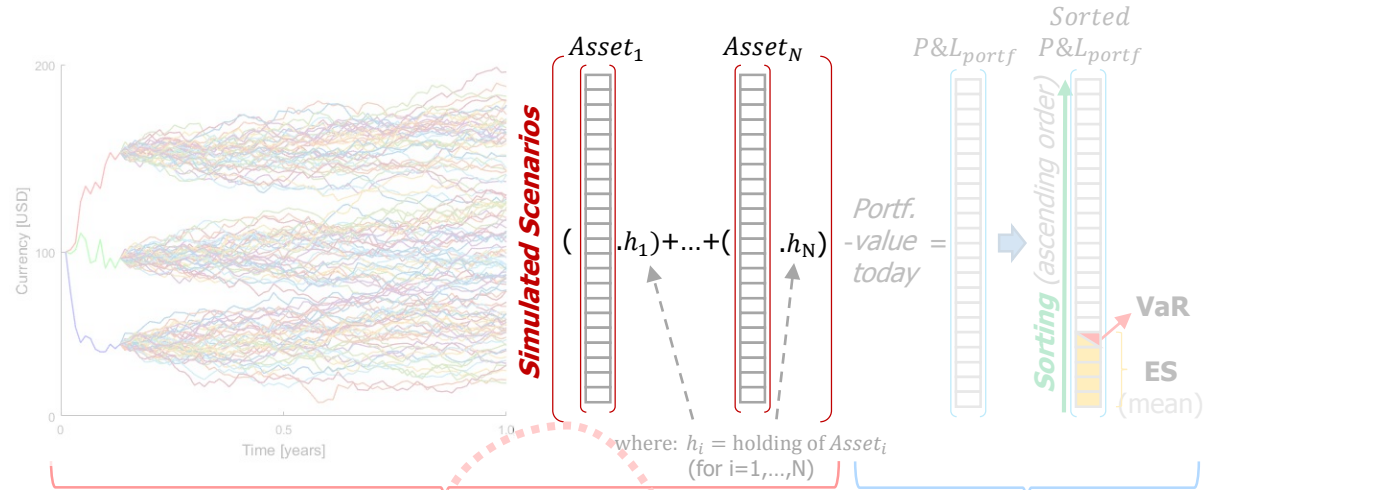
Minimizing Device Global Memory

Device Global Memory Cost (Kernels)



Minimizing Device Global Memory

Autoaggregation

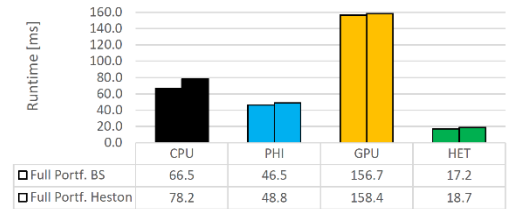
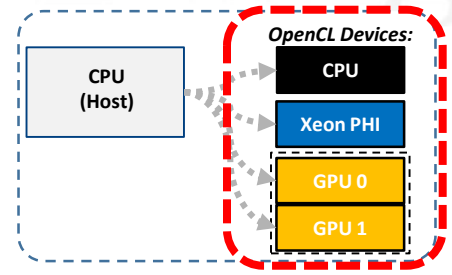
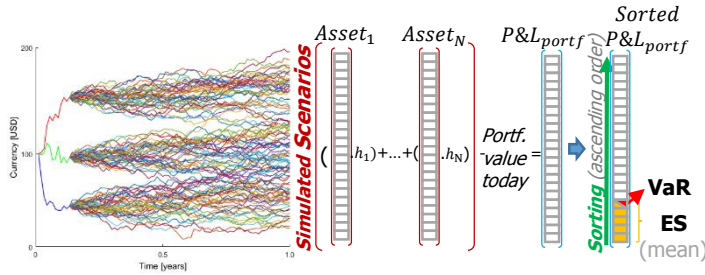


- ✓ Minimizes Host workload
- ✓ Minimizes data transfers via PCIe

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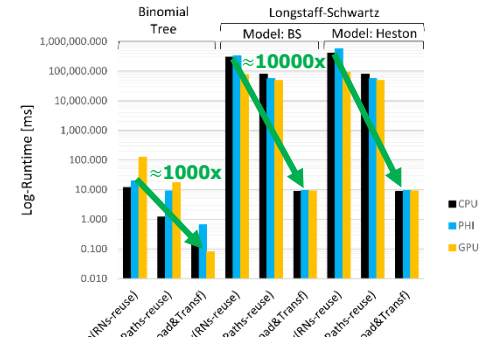
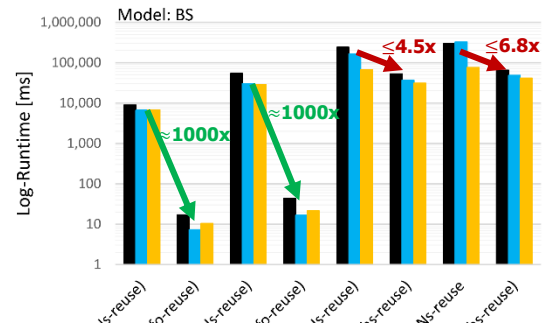
Conclusion

I. Overview: Portfolio Risk



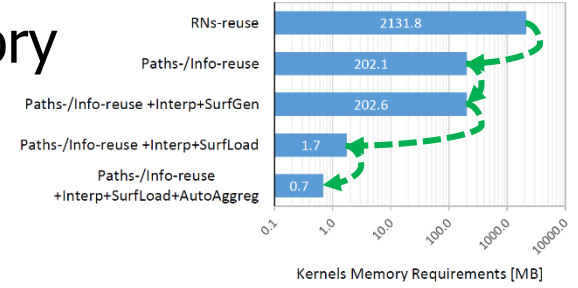
II. OpenCL + Workload Allocation

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IV. Minimizing Device Global Memory

Portability (Numerical Results)



Thanks for your attention